Nch 600V 7A Power MOSFET

V _{DSS}	600V
R _{DS(on)} (Max.)	0.62Ω
I _D	±7A
P _D	78W

Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Gate-source voltage (V_{GSS}) guaranteed to be ±20V.
- 4) Drive circuits can be simple.
- 5) Parallel use is easy.
- 6) Pb-free lead plating; RoHS compliant

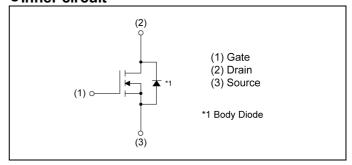
Application

Switching

Outline



•Inner circuit



Packaging specifications

Туре	Packing	Embossed Tape
	Reel size (mm)	330
	Tape width (mm)	24
	Basic ordering unit (pcs)	1000
	Taping code	TL
	Marking	R6007ENJ

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		V_{DSS}	600	V
Continuous dusin surrount	T _C = 25°C	I _D *1	±7	А
Continuous drain current	T _C = 100°C	I _D *1	±3.8	А
Pulsed drain current	·	I _{DP} *2	±14	А
Gate - Source voltage	static	\/	±20	V
	AC(f>1Hz)	V_{GSS}	±30	V
Avalanche current, repetitive		I _{AR}	1.3	А
Avalanche energy, single pulse		E _{AS} *3	133	mJ
Avalanche energy, repetitive		E _{AR} *3	0.20	mJ
Power dissipation (T _C = 25°C)	P _D *4	78	W	
Junction temperature	T _j	150	°C	
Operating junction and storage ten	nperature range	T _{stg}	-55 ~ +150	°C

● Absolute maximum ratings (T_a = 25°C)

Parameter	Symbol	Conditions	Values	Unit
Reverse diode dv/dt	dv/dt	-	15	V/ns
Drain - Source voltage slope	dv/dt	$V_{DS} = 480V, T_j = 25^{\circ}C$	50	V/ns

●Thermal resistance

Doromotor	Cymab al	Values			Lleit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC}	-	-	1.6	°C/W
Thermal resistance, junction - ambient	R _{thJA} *5	-	-	80	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
r ai ai netei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	600	-	1	V
		V _{DS} = 600V, V _{GS} = 0V				
Zero gate voltage drain current	I _{DSS}	T _j = 25°C	-	0.1	100	μA
		T _j = 125°C	ı	ı	1000	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} = 10V, I _D = 1mA	2	-	4	V
		V _{GS} = 10V, I _D = 2.4A				
Static drain - source on - state resistance	R _{DS(on)} *6	T _j = 25°C	-	0.57	0.62	Ω
on otato resistante		T _j = 125°C	-	1.20	-	
Gate resistance	R _G	f =1MHz, open drain	-	10.6	-	Ω

● Electrical characteristics (T_a = 25°C)

Davanastan	Curah al	Conditions		Values		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Transfer Admittance	Y _{fs} *6	V _{DS} = 10V, I _D = 3.5A	1.9	3.8	-	S
Input capacitance	C _{iss}	V _{GS} = 0V	-	390	-	
Output capacitance	C _{oss}	V _{DS} = 25V	-	390	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	50	-	
Effective output capacitance, energy related	C _{o(er)}	V _{GS} = 0V	-	21	-	F
Effective output capacitance, time related	C _{o(tr)}	V _{DS} = 0V to 480V	-	88	-	pF
Turn - on delay time	t _{d(on)} *6	V _{DD} ≃ 300V,V _{GS} = 10V	-	25	-	
Rise time	t _r *6	I _D = 3.5A	-	25	-	
Turn - off delay time	t _{d(off)} *6	R _L ≃ 86.6Ω	-	70	-	ns
Fall time	t _f *6	$R_G = 10\Omega$	-	35	-	

● Gate charge characteristics (T_a = 25°C)

Daramatar	O: make al	Conditions	Values			1.1:4
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Q _g *6	V _{DD} ≃ 300V,	-	20	-	
Gate - Source charge	Q _{gs} *6	I _D = 7.0A,	-	3	-	nC
Gate - Drain charge	Q _{gd} *6	V _{GS} = 10V	-	11	-	
Gate plateau voltage	V _(plateau)	V _{DD} = 300V, I _D = 7A	-	6.2	-	V

^{*1} Limited only by maximum channel temperature allowed.

^{*2} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*3} L \doteqdot 100mH, V_{DD}=50V, R_G=25 Ω , STARTING T_i=25 $^{\circ}$ C

^{*4} T_C=25°C

^{*5} Mounted on a epoxy PCB FR4 (25mm x 27mm x 0.8mm)

^{*6} Pulsed

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
raiametei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Continuous forward current	I _S *1	T _c = 25°C	1	1	7	А
Pulse forward current	l _{SP} *2	1 _c - 25 C	-	-	14	А
Forward voltage	V _{SD} *6	V _{GS} = 0V, I _S = 7.0A	-	-	1.5	V
Reverse recovery time	t _{rr} *6		-	340	-	ns
Reverse recovery charge	Q _{rr} *6	I _S = 7A, V _{GS} =0V di/dt = 100A/µs	1	2.8	1	μC
Peak reverse recovery current	I _{mm} *6	απατ 100, τμο	-	17	-	Α

● Typical Transient Thermal Characteristics

Symbol	Value	Unit	Symbol	Value	Unit
R _{th1}	0.172	_	C _{th1}	0.00102	
R _{th2}	0.694	K/W	C _{th2}	0.00288	Ws/K
R _{th3}	0.640		C _{th3}	0.153	

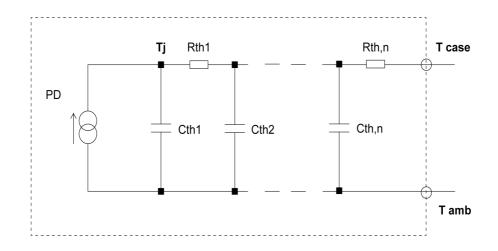


Fig.1 Power Dissipation Derating Curve

Fig.2 Normalized Transient Thermal Resistance vs. Pulse Width

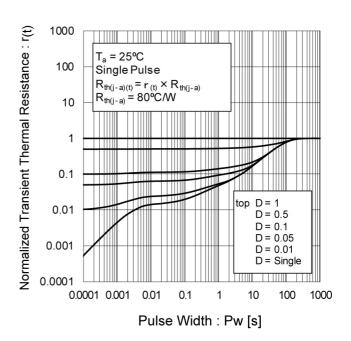
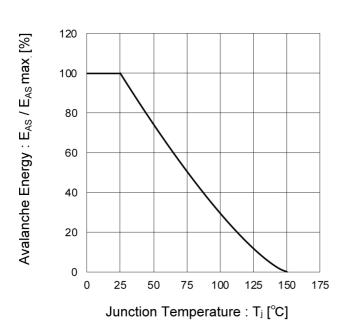


Fig.3 Avalanche Energy Derating
Curve vs. Junction Temperature

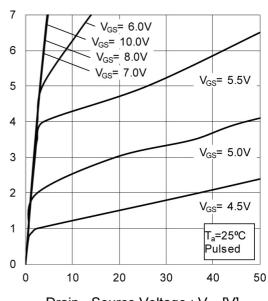


Drain Current : I_D [A]

• Electrical characteristic curves

Fig.4 Typical Output Characteristics(I)

Fig.5 Typical Output Characteristics(II)



Drain Current : I_D [A]

Drain - Source Voltage: V_{DS} [V]

Fig.6 Tj = 150°C Typical Output Characteristics (I)

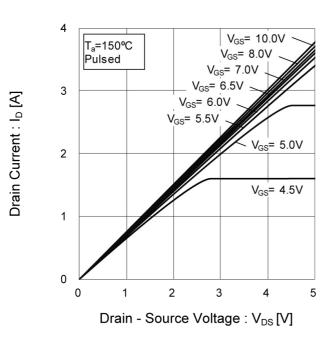
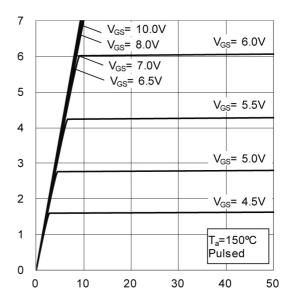


Fig.7 Tj = 150°C Typical Output Characteristics (II)



Drain - Source Voltage: V_{DS}[V]

Drain Current : I_D [A]

Fig.8 Breakdown Voltage vs.

Junction Temperature

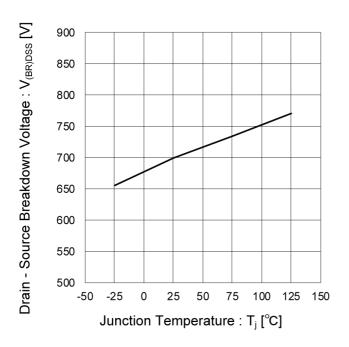


Fig.9 Typical Transfer Characteristics

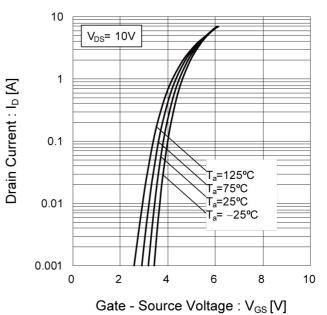


Fig.10 Gate Threshold Voltage vs.
Junction Temperature

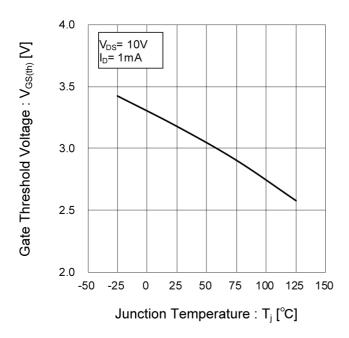


Fig.11 Forward Transfer Admittance vs.
Drain Current

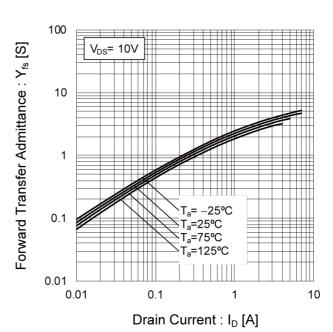


Fig.12 Static Drain - Source On - State
Resistance vs. Gate Source Voltage

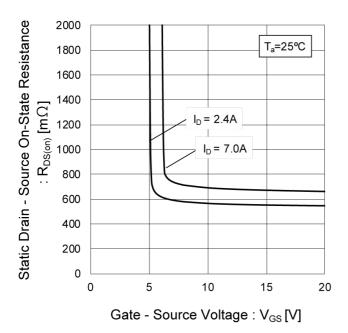


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

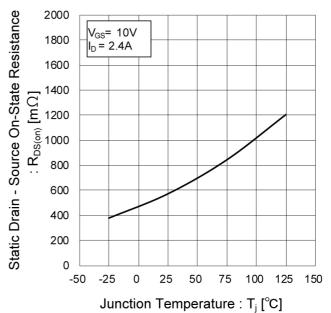


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

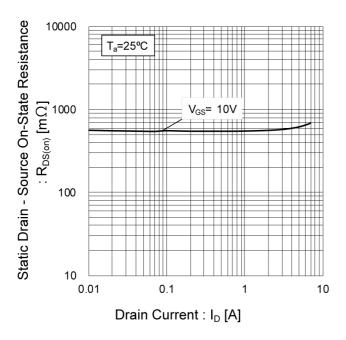
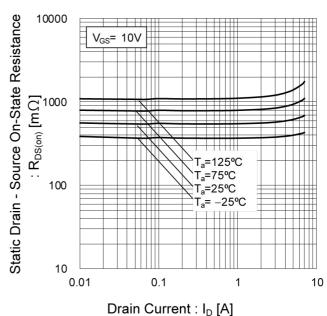


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)



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Fig.16 Typical Capacitance vs.

Drain - Source Voltage

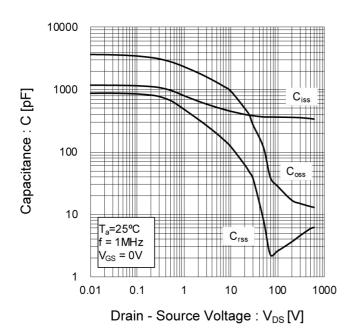


Fig.17 Coss Stored Energy

Coss Stored Energy : E_{oss} [uJ]

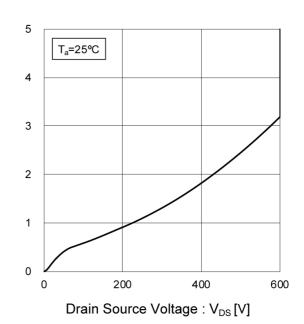


Fig.18 Switching Characteristics

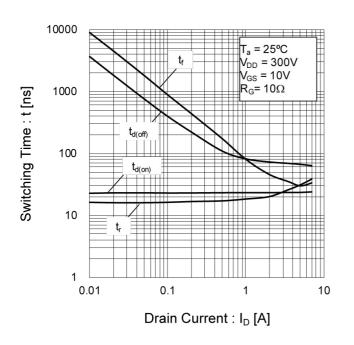
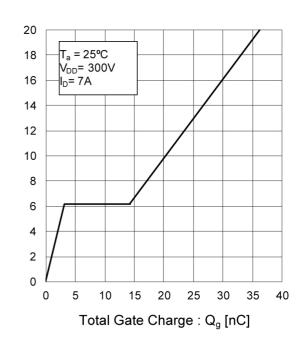
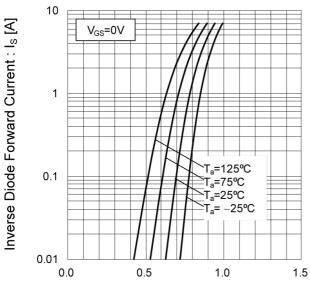


Fig.19 Dynamic Input Characteristics



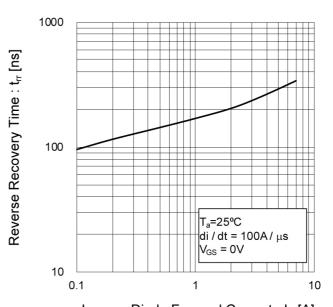
Gate - Source Voltage : V_{GS} [V]

Fig.20 Inverse Diode Forward Current vs. Source - Drain Voltage



Source - Drain Voltage : V_{SD} [V]

Fig.21 Reverse Recovery Time vs.
Inverse Diode Forward Current



Inverse Diode Forward Current : I_S [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

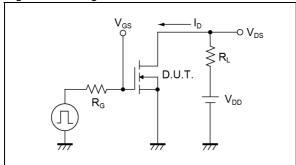


Fig.2-1 Gate Charge Measurement Circuit

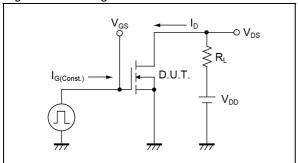


Fig.3-1 Avalanche Measurement Circuit

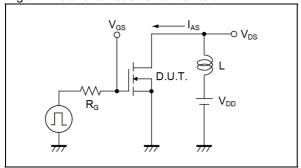


Fig.4-1 dv/dt Measurement Circuit

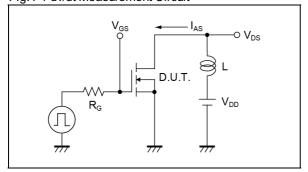


Fig.5-1 dv/dt Measurement Circuit

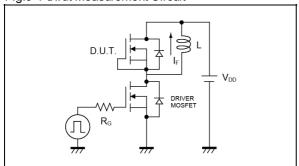


Fig.1-2 Switching Waveforms

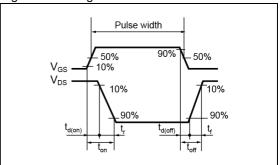


Fig.2-2 Gate Charge Waveform

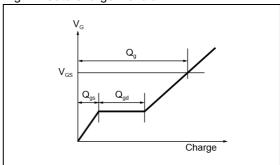


Fig.3-2 Avalanche Waveform

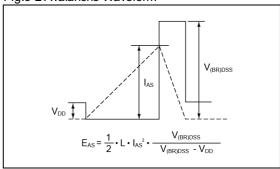


Fig.4-2 dv/dt Waveform

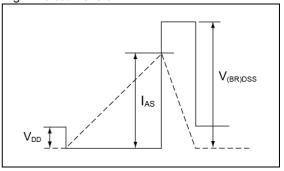
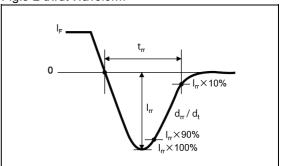
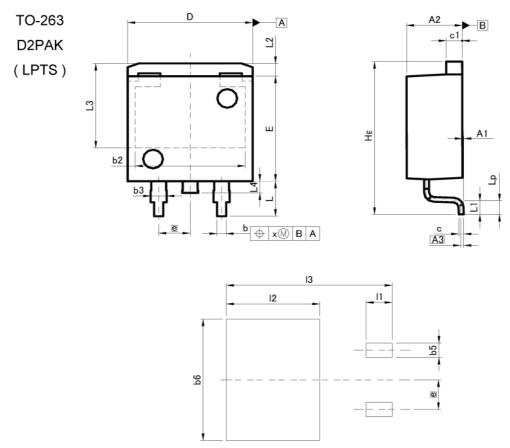


Fig.5-2 dv/dt Waveform



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

MILIM	ETERS	INC	HES
MIN	MAX	MIN	MAX
0.00	0.30	0.000	0.012
4.30	4.70	0.169	0.185
0.:	25	0.0	10
0.68	0.98	0.027	0.039
8.9	90	0.3	50
1.14	1.44	0.045	0.057
0.30	0.60	0.012	0.024
1.10	1.50	0.043	0.059
9.80	10.40	0.386	0.409
8.80	9.20	0.346	0.362
2.	54	0.100	
12.80	13.40	0.504	0.528
2.70	3.30	0.106	0.130
1.3	1.20		47
1.	10	0.0	43
7.:	25	0.2	85
1.0	00	0.0	39
0.90	1.50	0.035	0.059
-	0.25	-	0.010
			7) / / / / / / / / / / / / / / / / / / /
MILIM	ETERS	INC	HES
	MIN 0.00 4.30 0.68 8. 1.14 0.30 1.10 9.80 8.80 2.70 1. 1. 0.90 -	0.00	MIN MAX MIN 0.00 0.30 0.000 4.30 4.70 0.169 0.25 0.0 0.68 0.98 0.027 8.90 0.3 1.14 1.44 0.045 0.30 0.60 0.012 1.10 1.50 0.043 9.80 10.40 0.386 8.80 9.20 0.346 2.54 0.1 0.504 2.70 3.30 0.106 1.20 0.0 0.106 1.20 0.0 0.0 7.25 0.2 0.2 1.00 0.0 0.0 0.90 1.50 0.035 - 0.25 -

DIM	MILIM	MILIMETERS		HES
DIM	MIN	MAX	MIN	MAX
b5	-	1.23	-	0.049
b6	_	10.40	-	0.409
. 11		2.10	1	0.083
12	-	7.55	-	0.297
13		13.40	i —	0.528

Dimension in mm/inches



Notice

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(Note1) Medical Equipment Classification of the Specific Applications

JÁPAN	USA	EU	CHINA
CLASSⅢ	CLASSII	CLASS II b	CLASSIII
CLASSIV		CLASSⅢ	

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 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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